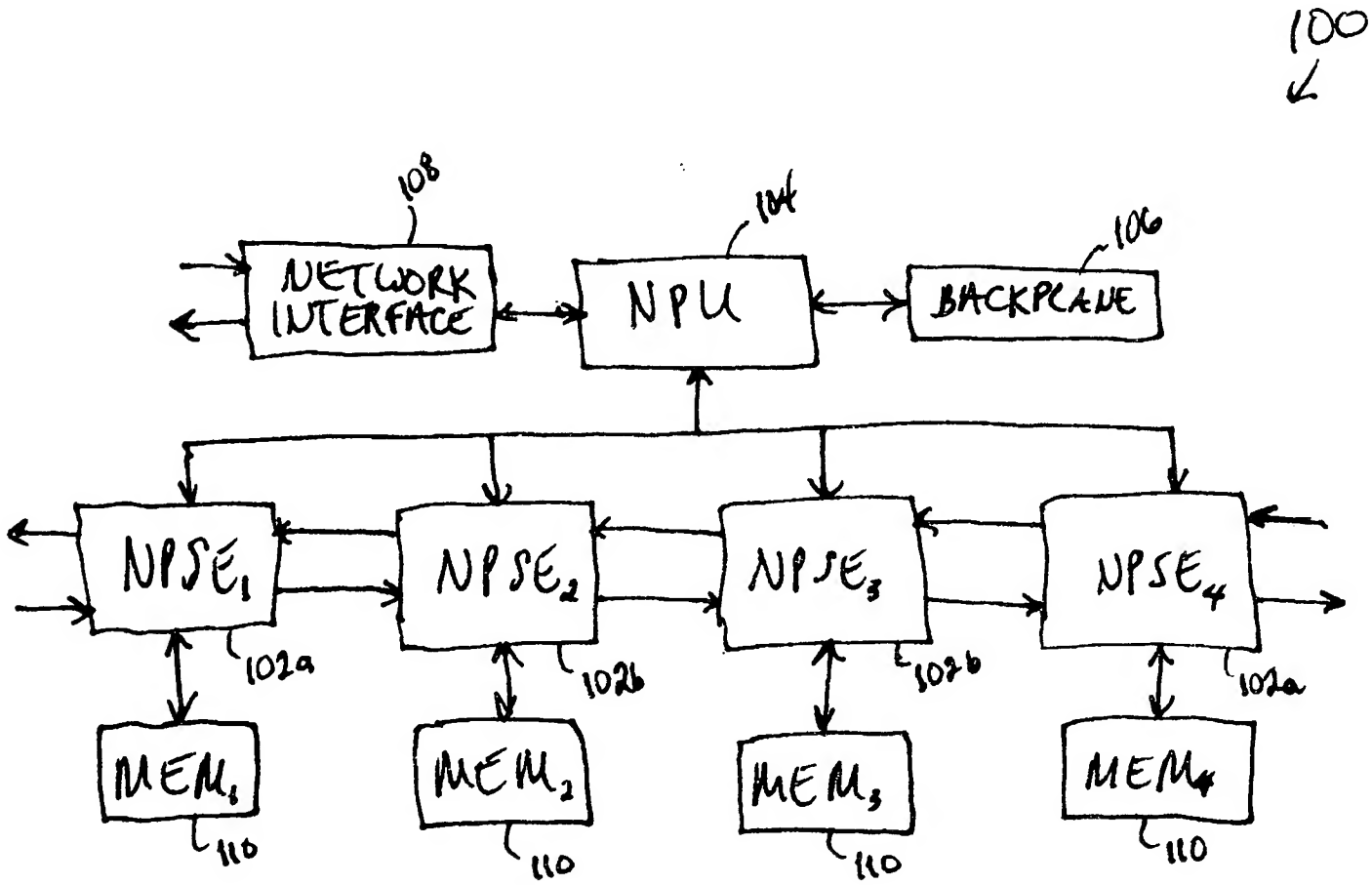
100
108
NETWORK INTERFACE
104
NPU
106
BACKPLANE
NPSE1
NPSE2
NPSE3
NPSE4
102a
102b
102b
102a
MEM1
MEM2
MEM3
MEM4
110
110
110
110

FIGURE 1

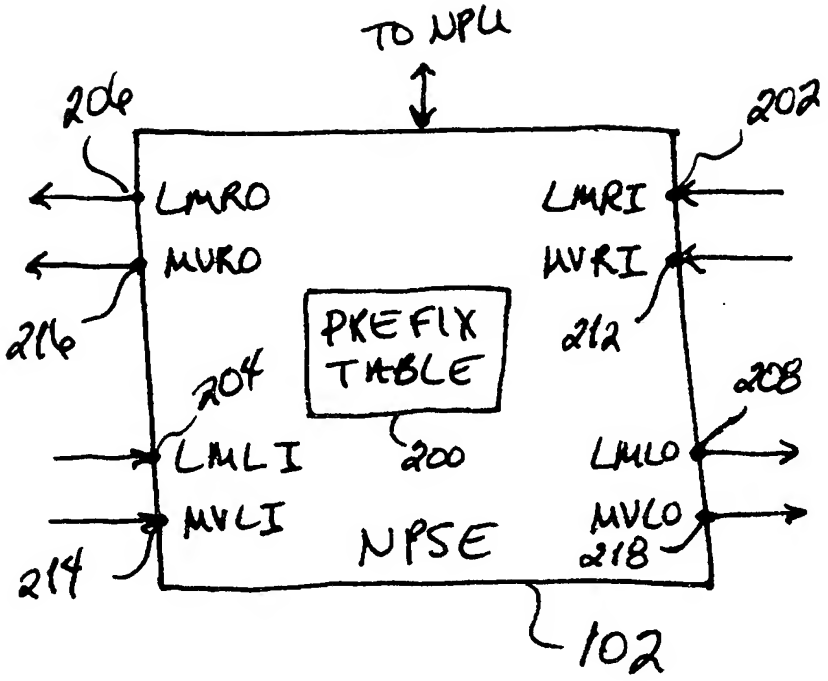
TO NPU
206
202
LMRO
LMRI
MVRO
MVRI
216
PREFIX TABLE
212
204
208
LMLI
200
LMLO
MVLI
NPSE
MVLO
214
218
102

FIGURE 2

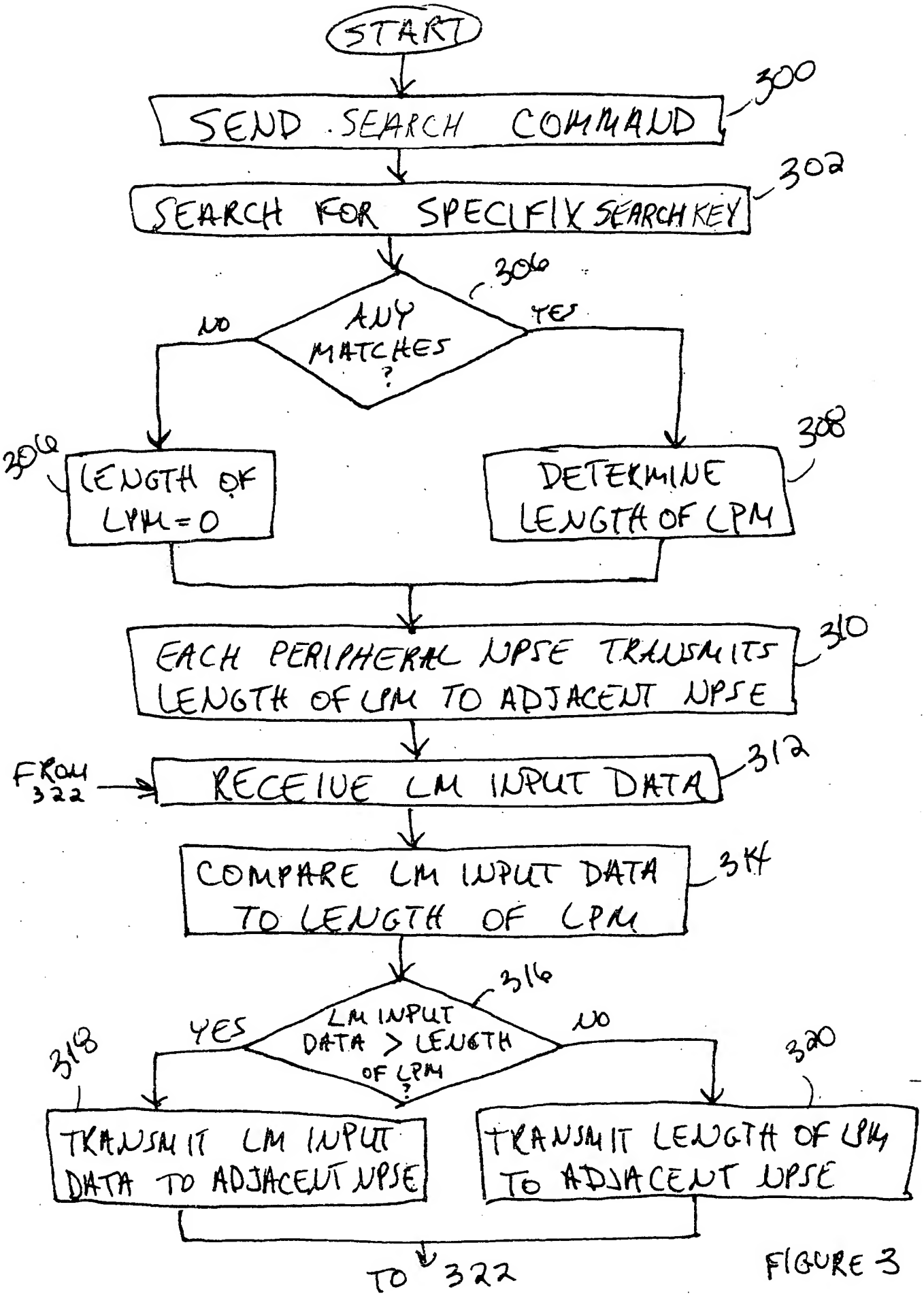
START
SEND SEARCH COMMAND
300
SEARCH FOR SPECIFIX SEARCH KEY
302
ANY MATCHES ?
306
NO
YES
LENGTH OF LPM = 0
304
DETERMINE LENGTH OF LPM
308
EACH PERIPHERAL NPSE TRANSMITS LENGTH OF LPM TO ADJACENT NPSE
310
FROM 322
RECEIVE LM INPUT DATA
312
COMPARE LM INPUT DATA TO LENGTH OF LPM
314
LM INPUT DATA > LENGTH OF LPM ?
316
YES
NO
TRANSMIT LM INPUT DATA TO ADJACENT NPSE
318
TRANSMIT LENGTH OF LPM TO ADJACENT NPSE
320
TO 322

FIGURE 3

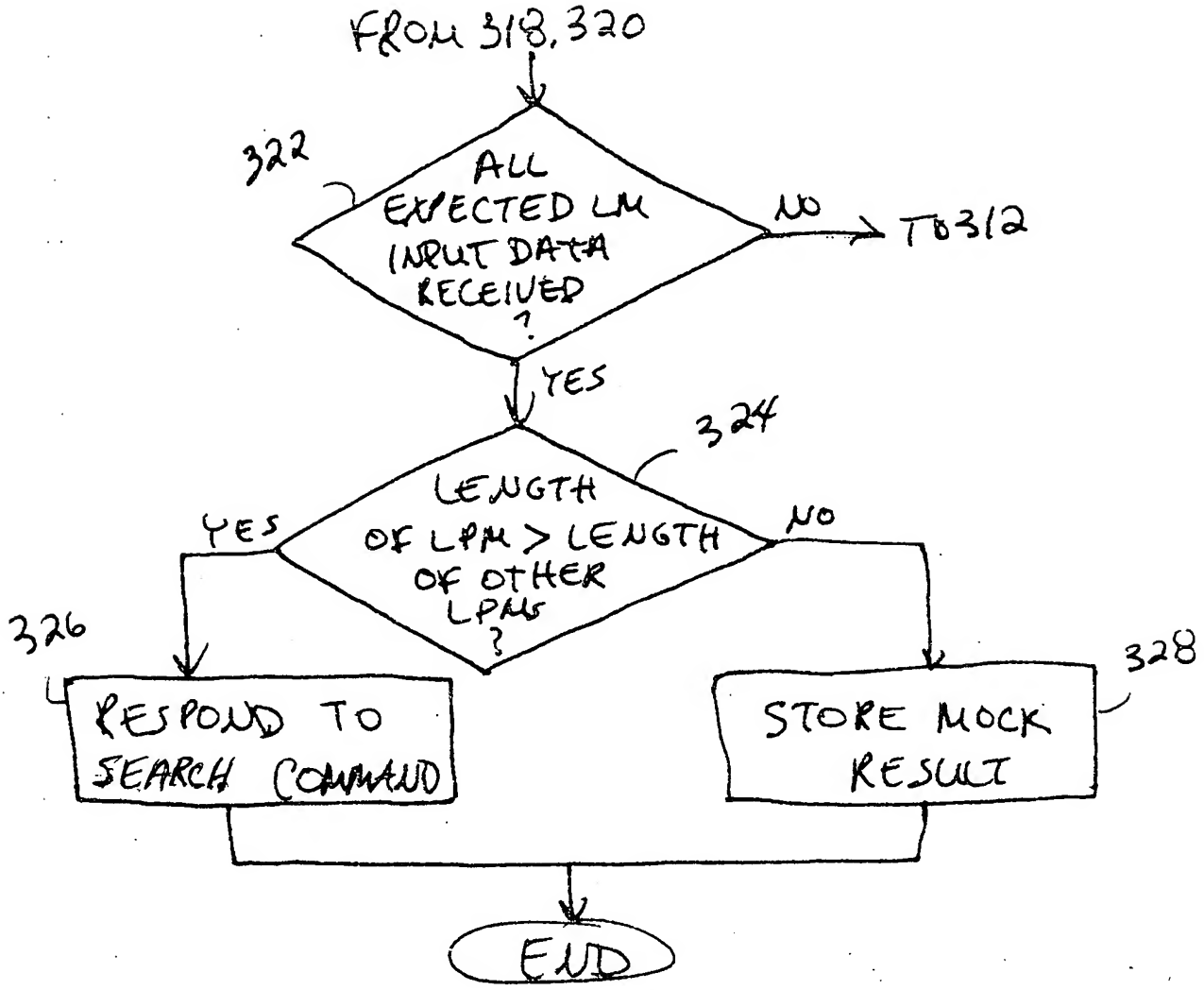
FROM 318, 320
322
ALL EXPECTED LM INPUT DATA RECEIVED ?
NO
TO 312
YES
324
LENGTH OF LPM > LENGTH OF OTHER LPMs ?
YES
NO
326
RESPOND TO SEARCH COMMAND
328
STORE MOCK RESULT
END

FIGURE 3